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| Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT | | | | Complete If Known | |
| | | | | Application Number | 10/037,666 |
| Sheet 2 of 2 | | | | Filing Date | January 3, 2002 |
| | | | | First Named Inventor | Sriram Vajapeyam |
| | | | | Art Unit | 2183 |
| | | | | Examiner Name | David I. Huisman |
| | | | | Attorney Docket Number | 42390P11927 |

| NON PATENT LITERATURE DOCUMENTS | | | |
|---------------------------------|-----------|---|----|
| Examiner Initials* | Cite No.¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T² |
| DJH | | Black, Bryan, et al., "The Block-based Trace Cache", Proceedings of the 26th International Symposium on Computer Architecture, pp. 196-207 (May 2-4, 1999) | |
| DJH | | Smith, James E., et al., "Trace Processors: Moving to Fourth-Generation Microarchitectures", Computer, Vol. 30, Issue 9, pp. 68-74 (Sep. 1997). | |
| DJH | | Vajapeyam, Sriram, et al., "Dynamic Vectorization: A Mechanism for Exploiting Far-Flung ILP in Ordinary Programs", Proceedings of the 26th International Symposium on Computer Architecture, pp. 16-27 (May 2-4, 1999) | |
| DJH | | U.S. Application No. 10/950,693, entitled SYSTEM, METHOD AND APPARATUS FOR DEPENDENCY CHAIN PROCESSING, filed September 28, 2004, by Satish Narayanasamy et al. | |
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| Examiner Signature | /David Huisman/ | Date Considered | 05/17/2006 |
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*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08B (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.
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